

Low Loss EELS Study of The Ultrathin SrTiO₃ Film Grown on The Si Single Crystal

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SrTiO₃ (STO) is a promising candidate for thin film applications in high-K capacitors and tunable microwave devices because of its high permittivity, low dielectric loss and high tunability. To make it reality, the growth of the STO film need to cooperate into the silicon based technology. Though the epitaxial STO films have been successfully grown on (001) silicon substrate ten years ago, the nature of interface and the growth mechanism are still not under investigation [1,2].

In this work, electron energy loss spectroscopy (EELS) carried out in a scanning transmission electron microscopy (STEM) has been used to study a Si/SrTiO₃/Si sandwich structural sample. Three-unit-cell thick STO layer was grown on (001) Si by MBE.[1] The epitaxy has been confirmed as (001)STO//(001)Si and STO[110]/Si[100] by XRD and electron diffraction. A 50nm amorphous Si was deposited on the top of STO film. We focus our study on the valence excitation region (Fig.1). We find that the interface plasmon excitation is not clearly visible for the STO thin film. The modes from both the bulk Si plasmon and the STO bulk Plasmon are found in STO film. The Si bulk plasmon in the STO layer is from the delocalized signal from the excitation of modes far from the electron trajectory[3,4]. The energy of Si bulk plasmon shifts 1 eV toward the low energy direction and the STO bulk plasmon shifts 1.4 eV toward the low energy direction(Fig.2). This result is different with the previous reports in Si/SiO₂ interface and Si/Co multi-layers [5, 6]. The shifts of plasmon peaks are considered as a interface effect. Our experimental data are simulated by the dielectric function theory [3]. Energy loss function, optical absorption, and surface loss function derived from the EELS image using a Kramers-Kronig analysis are shown in Fig.3. Our results provide information of local electronic excitation near the interface.

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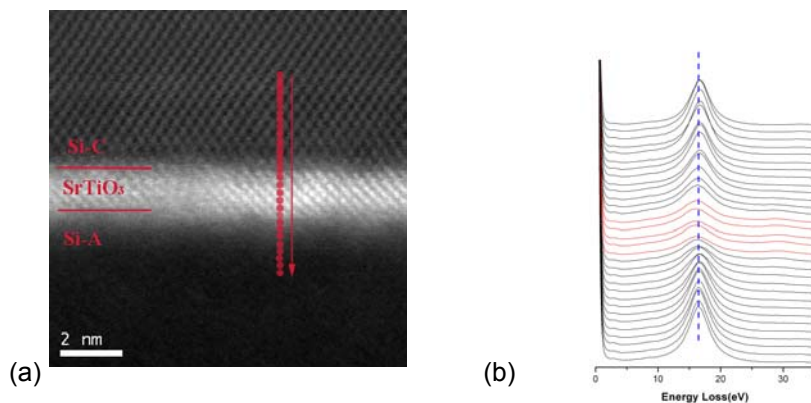


Fig.1 (a) A STEM_ADF image of Si/STO/Si sandwich structure. (b) a EELS scan along line marked in (a). The scan is from -2.5nm in Si substrate to the 2 nm in the amorphous Si layer.

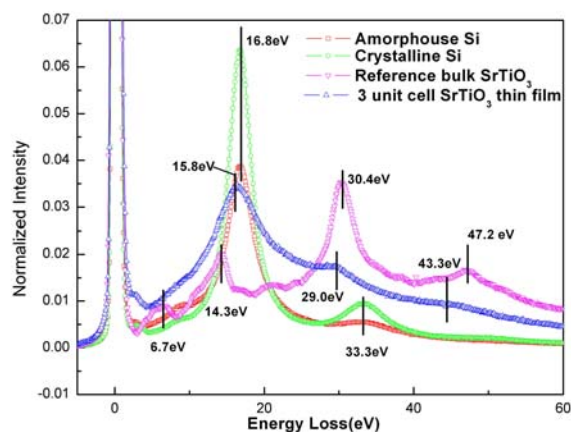


Fig.2 The low loss EELS from Si substrate, STO film and amorphous Si layer with longer acquire time. The low loss EELS of the bulk STO sample is also given as a reference.

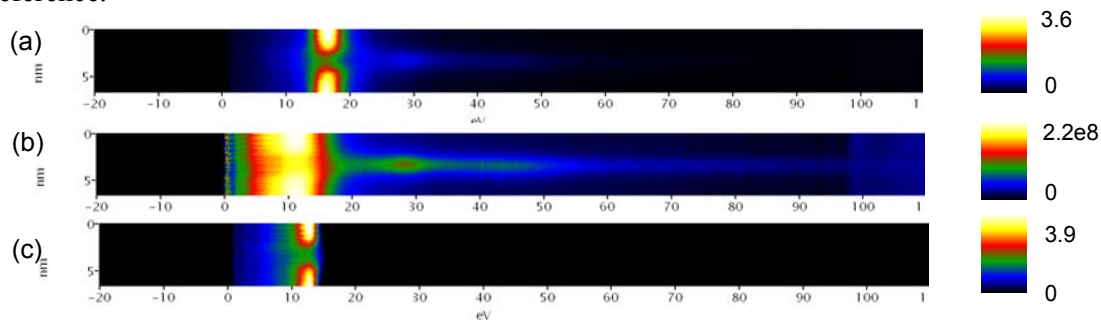


Fig.3 Energy loss function(a), Optical absorption(b), and Surface loss function derived from the line scan EELS image.