International Journal of Microwave and Wireless **Technologies**

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Research Paper

Cite this article: Mohamed HA, Ali WAE, Ibrahim AA (2023). LPF/dual-BPF/UWB-BPF reconfiguration utilizing DGS resonators and lumped capacitors. International Journal of Microwave and Wireless Technologies 15, 1108-1116. h[ttps://doi.org/10.1017/S1759078722001301](https://doi.org/10.1017/S1759078722001301)

Received: 12 May 2022 Revised: 27 October 2022 Accepted: 28 October 2022

Key words:

BPF; DGS; fan-shaped; interdigital capacitor; LPF; lumped capacitor

Author for correspondence: Ahmed A. Ibrahim,

E-mail: a[hmedabdel_monem@mu.edu.eg](mailto:ahmedabdel_monem@mu.edu.eg)

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LPF/dual-BPF/UWB-BPF reconfiguration utilizing DGS resonators and lumped capacitors

Hesham A. Mohamed¹ **D**[,](https://orcid.org/0000-0002-0136-7512) Wael A. E. Ali² **D** and Ahmed A. Ibrahim³ **D**

¹Electronics Research Institute, El-Nozha El-Gadida, Cairo 11843, Egypt; ²Arab Academy for Science, Technology & Maritime Transport (AASTMT), Alexandria, Egypt and ³Communications and Electronics Engineering Department, Minia University, Minia, Egypt

Abstract

In this work, three successive filter models based on defected ground structure (DGS) techniques are designed, fabricated, and tested to be utilized for different wireless applications. The filters are printed on a RO5880 substrate of an overall size of $22 \times 11 \times 0.8$ mm³. Three DGSs are etched from the ground plane to achieve the suggested band stop features for all frequencies above 10 GHz. The first model is a low-pass filter that succeeded to pass all frequencies below 10 GHz, the second model is a bandpass filter that operated at 4.5 and 9 GHz after using a pair of interdigital capacitors, and the third model covered all ultra-wideband frequency range when a pair of lumped capacitors is utilized instead of the interdigital capacitors. Good consistency between simulated and measured outcomes is achieved for each of the three filters confirming their suitability to be embedded in small-sized wireless devices.

Introduction

With the rapid development of wireless communications, there is an urgent need for designing microwave filters with various behaviors. More studies have been conducted to achieve low-pass response of the filter as well as the bandpass behavior to satisfy the demands of recent wireless technologies with constrained specifications like small occupied area, high selectivity, enhanced roll-off rate, wide rejection band, and low passband insertion loss [1[–](#page-7-0)[4\].](#page-7-0) There are different techniques to achieve the desired filter behavior such as defected ground structures (DGSs) for achieving wide stopband behavior [[5,](#page-7-0) [6\]](#page-7-0) and also it can be used to miniaturize the filter structure [[7\].](#page-7-0) Moreover, the stepped impedance resonator technique can be utilized in designing bandpass filters (BPFs) for highly independent multiband behavior [[8\],](#page-7-0) and for flexible bandwidth with an improved insertion loss [[9\].](#page-7-0) The substrate-integrated wave technique was utilized in [[10\]](#page-7-0) to achieve dual/triple passbands. In [[11\],](#page-7-0) interdigital capacitors are used to achieve the desired bandpass behavior with an extended rejection band and it is also utilized in the design of a BPF to cover the ultrawideband (UWB) frequency range from 3.1 to 10.6 GHz [[12\].](#page-7-0) Two BPFs are introduced in [[13,](#page-7-0) [14\]](#page-7-0) based on the open-loop resonator technique for WLAN/WiMAX applications. Tunable filters are considered one of the demanded filters due to their vital role in adaptation to the requirements of wireless systems such as reconfigurable or cognitive systems. Recently, many tunable filters have been reported such as the reported Band stop filter (BSF) in [[15\]](#page-7-0) which is based on a tunable epsilon-shaped metamaterial using a lumped capacitor (connected in series) to achieve triple-band operation, also the same kind of lumped capacitors (connected in shunt) can shift the obtained bandpass behavior to the lowerfrequency range causing a reduction in the filter size to more than 35% as in [[16\].](#page-7-0) The reconfigurability of the suggested filters in [[17,](#page-7-0) [18\]](#page-7-0) is carried out using PIN diodes to either obtain dual-mode behavior or control the notched band, respectively.

In this paper, a DGS-based low-pass filter (LPF) is introduced to achieve a larger frequency range up to 10 GHz and this is done by three etched DGS shapes from the ground plane which operated as band stop filters in the range from 10 to 18 GHz. Furthermore, two interdigital capacitors are inserted in the feedline of the aforementioned designed filter to obtain a dual passband behavior at 4.5 and 9 GHz. In order to achieve a wideband instead of the dual narrow bands when using two interdigital capacitors, two lumped capacitors are embedded and the exact values are chosen after carrying out a parametric study on capacitor values; finally two pairs of 1 pF capacitors are chosen to cover a larger frequency range starting from 1.9 to 10 GHz with a low-level return loss in the achieved passband range. Finally, the performance of each filter of the three suggested filters is validated experimentally and it was confirmed that the results of the fabricated filters and their simulated outcomes confirm the ability of the implemented filter to be incorporated in modern wireless devices due to their competitive specifications with the reported filters.

Fig. 1. S_{21} response of different DGS shapes.

DGS BSF (Band stop filter)/LPF

In this section, BSF/LPF based on DGS shapes is investigated. The DGS shapes are etched in the ground plane to achieve the proposed band-stop features. To achieve the proposed LPF from BSF, multiple attenuation poles should be used. So, different DGS shapes with different sizes should be used. Figure 1 shows the S_{21} response of the DGS shapes. The fan-shaped with a larger size is etched in the ground plane while the microstrip line with 50 Ω is used on the other side of the substrate. The filter has band stop behavior with attenuation pole (f_p) and 3-dB cut-off frequency (f_c) corresponding to 14 and 11.7 GHz (the red-dashed line). Furthermore, to increase the attenuation poles in the

stopband operation, DGSs with I-shaped and two backed C-shaped with smaller sizes are used as shown in Fig. 1. The filter has a band stop feature with two attenuation poles (f_n) and 3-dB cut-off frequency (f_c) corresponding to 13.25, 16, and 11.5 GHz for both bands (the solid black line). Finally, the smaller DGS with fan-shaped can be used to achieve band stop behavior with attenuation pole (f_p) and 3-dB cut-off frequency (f_c) corresponding to 18 and 16.2 GHz (the blue-dotted line). The parallel LC circuit can be used to model the DGS BSF. The values of the L and C can be extracted using the below equations [\[19](#page-7-0), [20](#page-7-0)]:

$$
C_{\rm S} = \frac{5f_c}{\pi (f_p^2 - f_c^2)} \text{ pF}
$$
 (1)

$$
L_S = \frac{250}{C_s(\pi f_p)^2} \text{ nH}
$$
 (2)

So, by combining the three DGS shapes as shown in Fig. 2, the LPF from BSF can be generated as shown in [Fig. 3](#page-2-0). The threedimensional and two-dimensional views with the dimensions are shown in Fig. 2. The filter has three DGS-shaped etched on the ground of the substrate while a microstrip line with 50 Ω is printed on the top of the Roger 5880 substrate with $\varepsilon_r = 2.2$ and thickness of 0.8 mm.

The equivalent circuit model of the proposed LPF is shown in [Fig. 3\(a](#page-2-0)). As discussed before the DGS element can be modeled as a parallel LC circuit, so four LC circuits are used to model the DGS BSF shape as discussed above. The comparison between the S-parameter responses of the electromagnetic (EM) and circuit simulation is shown in Fig. $3(b)$. It is obvious that the filter

Fig. 2. DGS LPF configuration with $L = 11$ mm, $W = 22$ mm, $S_1 = 0.2$ mm, $S_2 = 0.1$ mm, $L_1 = 5.5$ mm, $L_2 = 2.95$ mm, $L_3 = 1.8$ mm, $L_4 = 2.4$ mm, and $W_f = 2.4$ mm.

Fig. 3. DGS LPF equivalent circuit analysis. (a) Equivalent circuit model with L_{S1} = 0.41 nH, C_{S1} = 0.315 pF, L_{S2} = 0.35 nH, C_{S2} = 0.42 pF, L_{S3} = 0.66 nH, C_{S3} = 0.147 pF, L_{S4} = 0.186 nH, C_{S4} = 0.418 pF, C_P = 0.316 pF. (b) S-parameter responses of the EM simulation and circuit simulation.

is LPF with passband operation up to 10.6 GHz and has wide stopband operation up to 16 GHz because of the multiple attenuation poles achieved by the DGS resonators. Also, a good trend between the two results is achieved. The proposed filter is fabricated and tested, and its fabricated photograph is illustrated in Fig. 4.

The filter is tested using a test feature kit instead of soldering two SMA connectors as shown in Fig. 4. The proposed filter is

tested using a Rohde & Schwarz ZVA 67 vector network analyzer using 50- Ω ports to extract the filter's S-parameter response as shown in Fig. 5. It is clear from the measured results that the filter is LPF with a passband extended up to 10 GHz and with a stopband lower than −10 dB up to more than 16 GHz. Also,

Fig. 4. Top and back views of the DGS LPF fabricated prototype. Fig. 5. S-parameter results of the simulated and tested of the proposed DGS LPF.

 L_5 = 1.8 mm, L_6 = 2.4 mm, L_7 = 2.1 mm, and W_1 = 1.4 mm.

TOP mn cm

Fig. 8. Top and back views of the DGS BPF fabricated prototype.

there is a shift between the tested and simulated results due to the fabrication process and the small gap dimensions of the cells around 0.1 mm (human error) which can make some errors in the testing.

LPF to dual and narrow bands DGS BPF transformation

In this section, LPF to BPF transformation is discussed using an interdigital capacitor as a coupling element instead of using a gap J-inverter [\[1](#page-7-0)]. The configuration of the proposed with an optimized dimension of the BPF is shown in Fig. 6. The coupling element is used to suppress the lower- and upper-frequency

Fig. 7. DGS narrow bands BPF equivalent circuit analysis. (a) Equivalent circuit model with $L_{S1} = 0.41$ nH, $C_{S1} = 0.315$ pF, $L_{S2} = 0.35$ nH, $C_{S2} = 0.42$ pF, $L_{S3} = 0.66$ nH, $C_{S3} = 0.147 \text{ pF}$, $L_{S4} = 0.186 \text{ nH}$, $C_{S4} = 0.418 \text{ pF}$, $C_P = 0.316 \text{ pF}$, $C_{C1} = C_{C2} = 0.34 \text{ pF}$, $L_{C1} = 1.45 \text{ nH}$, $L_{C2} = 1.54 \text{ nH}$, $C_{P1} = 1.39 \text{ nH}$, and $C_{P2} = C_{P3} = C_{P4} = 0.939 \text{ pF}$. (b) S-parameter responses of the EM simulation and circuit simulation.

bands of the signal. The same previous substrate is used in the design. The capacitance of the interdigital capacitor is calculated as [[21\]](#page-7-0)

$$
C_L = 3.937 \times 10^{-5} L_C(\varepsilon_r + 1)(0.11(n-3) + 0.252) \text{ pF} \qquad (3)
$$

where, C_L , L_C , and *n* are the capacitance, length, and the number of fingers of the interdigital capacitor.

The value of the capacitance of the interdigital capacitor can be controlled using its dimensions. So, the center frequency of the dual bands can be controlled by changing its dimensions. The interdigital capacitor can be modeled using the π section as illustrated in [Fig. 7](#page-3-0) [[22\].](#page-7-0) The equivalent circuit model of the proposed BPF is illustrated in [Fig. 7\(a\)](#page-3-0) and the EM simulation in comparison with the circuit simulation is illustrated in [Fig. 7\(b\).](#page-3-0) It is noticed that the filter is operated in two frequency bands with a center frequency of 4.5 and 9 GHz. The S_{21} levels equal 2.1 and 2.2 dB, respectively. The stopband levels between the two bands are lower than −15 dB. The filter has a stopband lower than −15 dB from 10 up to 16 GHz. Also, a good trend is achieved between the two simulators.

The proposed BPF is fabricated and tested, and its fabricated photograph is illustrated in [Fig. 8.](#page-3-0) It is seen from the measured results that the filter is BPF with dual and narrow frequency bands operated at a center frequency of 4.5 and 9 GHz, respectively. The S_{21} levels of the passbands equal 2.2 dB in both the bands. The stopband levels between the two bands are lower than −13 dB. The filter has a stopband lower than −15 dB from 10 up to 16 GHz. Also, good matching is obtained between the simulated and tested outcomes as illustrated in Fig. 9.

Proposed wide-band DGS BPF

To achieve wideband operation, enhance the filter response, and achieve compact size, lumped capacitors are used as coupling elements between the input/output ports as illustrated in Fig. 10. The lumped capacitors are used to suppress the lower band frequency. The lower band suppression is controlled by changing the lumped capacitor capacitance as shown in Fig. 11. From Fig. 11 it is clear that by increasing the capacitance of the lumped capacitor from 0.5, 1, and 1.5 pF, the lower-frequency band can be shifted down from 3.5 to 2 and 1.5 GHz respectively. Also, the passband

Fig. 10. DGS wide-band BPF configuration with lumped capacitors.

Fig. 11. S-parameter responses of DGS wide-band BPF configuration with different values of lumped capacitors.

and reflection coefficient responses are enhanced by increasing the capacitance values. The capacitance of 1 pF is used in the fabrication.

The equivalent circuit model of the proposed BPF is illustrated in Fig. $12(a)$ and the EM simulation in comparison with the circuit simulation is illustrated in Fig. $12(b)$. It is noticed that the filter has a wide band operation with a bandpass feature extended from 2 up to 10.6 GHz. The S_{21} levels of the passband equal 0.5 dB. The filter has a stopband lower than −15 dB from 11 up to 16 GHz. Also, a good trend is achieved between the two simulators. The electric field distribution of the proposed filter at 8 and 14 GHz inside the substrate is illustrated in [Fig. 13.](#page-5-0) At 8 GHz (passband) the electric field is transferred from port 1 to port 2 while at 14 GHz (stopband) the electric field did not reach port 2.

The proposed BPF is fabricated and tested, and its fabricated photograph is illustrated in [Fig. 14.](#page-6-0) It is obvious from the tested results that the filter has a wideband operation with a passband starting from 1.9 up to 10 GHz as illustrated in [Fig. 15.](#page-6-0) The S_{21} levels of the passband equal 0.8 dB. The fil-Fig. 9. S-parameters of the simulated and tested results of the proposed DGS BPF. ter has a stopband lower than −15 dB from 10 up to 16 GHz.

Fig. 12. DGS wide-band BPF equivalent circuit analysis. (a) Equivalent circuit model with L_{S1} = 0.41 nH, C_{S1} = 0.315 pF, L_{S2} = 0.35 nH, C_{S2} = 0.42 pF, L_{S3} = 0.66 nH, C_{S3} = 0.147 pF, L_{S4} = 0.186 nH, C_{S4} = 0.418 pF, C_P = 0.316 pF, and C_C = 1 pF. (b) S-parameter responses of the EM simulation and circuit simulation.

Fig. 13. Distribution of the electric field inside the substrate at port 1 of the DGS wide-band BPF: (a) at 8 GHz and (b) at 14 GHz.

 $11497 +$ $9020 -$ 7028 $5426 4138 3103 2270 -$ 1600 $1061 -$

> 628 280 $\bf{0}$

Fig. 14. Top and back views of the DGS wideband BPF fabricated prototype.

Fig. 15. S-parameters of the simulated and tested results of the proposed wideband DGS BPF.

Also, good matching is obtained between the simulated and tested outcomes. However, a small shift is noticed especially at the upper-frequency band due to the reasons mentioned above in the "LPF " Section. Finally, it is concluded that BPF responses are achieved by using LPF responses without changing the filter size.

Table 1 compares the suggested filters with the others. The suggested filters have a compact size with high performance which makes it to be a good choice for small-size wireless applications.

Conclusion

In this paper, three compact LPF/dual-BPF/UWB-BPF of a size of $22 \times 11 \times 0.8$ mm³ have been designed, fabricated, and then measured to validate their simulated outcomes. LPF behavior (0 –10 GHz) has been achieved after etching three DGSs from the ground plane. Furthermore, a pair of interdigital and lumped capacitors has been utilized to achieve the dual-band (4.5 and 9 GHz) and the extended UWB (1.9 –10 GHz) operations, respectively. The obtained results have verified that the suggested filters have advantages in insertion loss, selectivity, and wide stopband which are very attractive features in modern wireless systems.

Data

There are no supplementary materials, and the data are available upon reasonable request.

Author contributions. All authors contributed equally to analyzing data and reaching conclusions, and in writing of this paper.

Financial support. No funds, grants, or other support were received to conduct this study.

Conflict of interest. The authors report no conflict of interest.

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Hesham A. Mohamed received his B.Sc. in electronics and communication engineering from the University of Menofia in 2003 and his M.Sc. and Ph.D. from Ain Shams University in 2009 and 2014, respectively. He is currently an associate professor at Electronics Research Institute (ERI), Giza, Egypt. Dr. Hesham has teaching experience (more than 12 years) as lecturer in Electronic and Communication

Engineering Department in Faculty of Engineering Misr University for Science and Technology. Dr. Hesham is an SMIEEE senior member of the IEEE (Institute of Electrical and Electronic Engineers). He has authored or co-authored nearly 30 journal papers, about 17 refereed conference papers, and has attended and chaired several national and international conferences. He has supervised and co-supervised four Ph.D. and four M.Sc. theses at the Cairo University, Ain Shams University, Benha University, and Helwan University. His outstanding works have been published in international journals and conferences. He is a reviewer of the following international journals: IEEE Microwave and Wireless Components Letters, IEEE Access, Progress in Electromagnetics Research (PIER, PIER B, C, M, PIER Letters), Microwave and Optical Technology Letters, and International Journal of Circuit Theory and Applications. Dr. Hesham participates in more than six research projects at the national and international levels such as Egypt-NSF-USA joint fund program, Egypt STDF-France IRD joint fund program, and the European Committee programs of FP6 and FP7. His role in the following projects is from C-PI to PI: as an RF-design engineer (antenna and power amplifier module) (2018–2020) in the project "Small SAR satellite antenna and transceiver system," a communication subsystem engineer (2019–2020) in the project "Egyptian University Satellite (EUS-2)," an RF-design engineer (2018–2019) in the project "Design of Radar Absorbing Materials (RAM) using Meta-materials," a wireless system engineer (2015–2018) in the project "Development of High Data Rate X-Band Transmitter for LEO Remote Sensing Satellites," an RF-design engineer (2015–2018) in the project "The Egyption 2D Radar," an RF-design engineer (2011–2014) in the project "Ultra-wideband Ground Penetrating Radar for Water Detection in Egypt," and an RF-design engineer (February 2011–February 2014) in the project "Novel Planar Antennas for the Most Recent Telecommunications Applications." His research interests include microwave circuit designs, planar antenna systems, recently on EBG structures, UWB components and antenna and RFID systems, radar absorbing materials, energy harvesting and wireless power transfer, smart antennas, microstrip antennas, microwave filters, metamaterials, and MIMO antennas and its applications in wireless communications.

Wael A. E. Ali was born in 1982. He received his B.Sc. and M.Sc. in electronics and communications engineering from Arab Academy for Science, Technology and Maritime Transport (AASTMT), Alexandria, Egypt in 2004 and 2007, respectively. He obtained his Ph.D. in electronics and communications engineering from Alexandria University, Alexandria, Egypt in 2012. He is currently a professor at Arab

Academy for Science, Technology and Maritime Transport (AASTMT), Alexandria, Egypt. He has published more than 82 peer-reviewed journal and conference papers. His research interests include smart antennas, microstrip antennas, microwave filters, metamaterials, millimetric-wave antennas, and MIMO antennas for various wireless communications applications.

Ahmed A. Ibrahim (M'19–SM'20) was born in 1986. He received the B.Sc., M.Sc., and Ph.D. in electrical engineering from the Electronic and Communication Engineering Department, Minia University, El-Mina, Egypt in 2007, 2011, and 2014, respectively. He is now an associated professor in the Electrical Engineering Department in the Faculty of Engineering, Minia University. He has been a visiting profes-

sor at University Pierre and Marie Curie, Sorbonne University, Paris VI,

France for 7 months and Otto-von-Guericke-Universität Magdeburg-Germany for 6 months. He has published more than 95 peer-reviewed journal and conference papers. His research has focused on miniaturized multiband antennas/ wideband, microwave/millimeter components, DRA metamaterial antennas, graphene antennas, and microwave filters. Also, his research includes MIMO antennas and energy harvesting systems. Dr. Ahmed A. Ibrahim is a senior member of the IEEE and a senior member in URSI and also a member of the national committee of radio science in Egypt. He is currently a reviewer of IEEE Antennas and Wireless Propagation Letters, IEEE Microwave Wireless Components, IEEE Access, IET Microwave, Antenna and Propagation, IET Electronics Letters, MOTL, Analog Integrated Circuits, and Signal Processing, and many other journals and conferences. In 2020 and 2021, he was named in the top 2% of scientists in "A standardized citation metrics author database annotated for scientific field/Updated science-wide author databases of standardized citation indicators."