High Resolution TEM Observation of Nanocrystalline Silicon Fabricated by High Pressure Torsion (HPT)

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Silicon has 12 different crystal structures under different pressure [1]. Si has the cubic diamond (cd) structure (Si-I) under pressure up to ~11.7 GPa. Si-I transforms to the metallic β -tin structure (Si-II) above ~11.7 GPa. Si-II transforms to the R8 structure (Si-XII) at ~8 GPa to Si-I when pressure is released slowly. Si-XII may transform subsequently to the BC8 structure (Si-III) upon releasing the pressure. Si-XII and Si-III transform to the hexagonal diamond (hd) phase (Si-IV) by subsequent annealing at moderate temperature [1,2]. Si-IV also can transforms to Si-I by prolonged annealing. In this study, the effect of severe plastic deformation (SPD) on pressure-induced phase transformations is investigated using the high-pressure torsion (HPT) method, which is originally used to improve the mechanical properties of alloys by producing ultrafine-grained (UFG) structures [3].

The HPT facility used in this study consisted of two anvils with a hole (5 mm diameter and 0.25 mm thickness) on the surface of each anvil. Two Si (100) wafers were placed on the hole of the lower anvil, and then slowly compressed under a pressure of 24 GPa. After compression, the lower anvil was rotated with respect to the upper anvil for 20 turns using a rotation speed of 1 rpm at room temperature. After HPT processing, some samples were annealed at 873 K for 1, 2, 6, 12 hours under N₂ flow (0.1 L/m). For transmission electron microscopy (TEM), samples were prepared by crushing in isopropanol and deposition onto holey carbon films. Nanostructures were examined by phase-contrast high-resolution TEM using a JEOL JEM-4000EX operated at 400 keV and a FEI-Philips CM200-FEG operated at 200 keV. High-resolution images were analyzed using Gatan DigitalMicrograph software.

Examination of TEM images indicates that multiple metastable high-pressure phases are formed by HPT processing. Figure 1(a) shows the presence of Si-XII phase with orientations of [112] and [0-1-1]. Other diffraction spots in the diffractogram of Fig. 1(a) correspond to Si-I phase. Figure 1(b) suggests the coexistence of Si-I and Si-IV with orientations of [110] and [100], respectively. The presence of many spots in the diffractogram of Fig. 1(a) and (b) indicates the existence of small nanograins with sizes in the nanometer scale. The grain boundaries are shown more clearly in Fig. 1(b). After annealing, Si-I is the only detected phase, consistent with an earlier report [4]. The grain sizes remain in the nanometer range after annealing and many dislocations, as shown in Fig. 2(a), and many nanotwins with a typical lamella width in the range of 2-3 {111} atomic fringes, as shown in Fig. 2(b), are formed. These lattice defects also form after HPT processing, but their fraction increases after annealing [5].

References:

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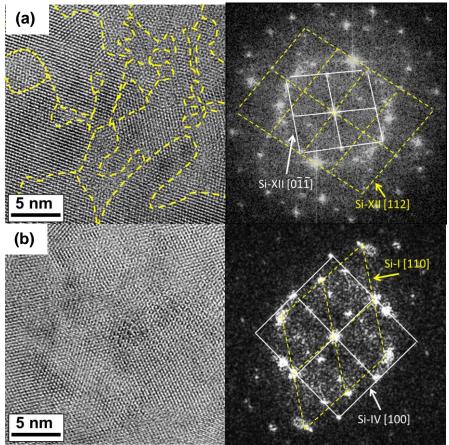


Figure 1. TEM lattice images (left), and corresponding diffractograms (right), for Si after HPT processing. Formation of metastable Si-XII and Si-IV are shown in (a) and (b), respectively.

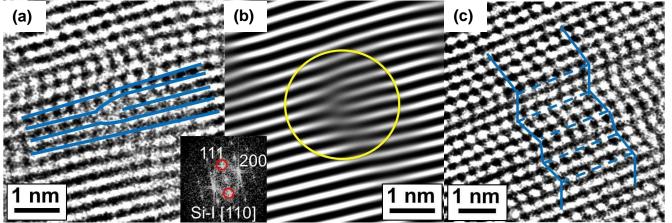


Figure 2. (a) Lattice image of {111} dislocation and (b) corresponding inverse FFT constructed using [111] diffraction spots. (c) Lattice image of Si nanotwins present after annealing.